Short Presentation of a New German Project
for True Random Number Generation on FPGAs

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On April 1 of 2011, a research project on true random number generation on FPGA with German national funding was started. The consortium consists of:

- Technical University of Dresden

- secunet Security Networks AG

- Siemens AG

Unfortunately, the project has not yet agreed on a suitable acronym.

In the presentation, the aims and scheduled work packages of the project will be presented.

Note: This is not a regular submission, there are no results yet. A 5 minutes slot would be perfect to present the project.