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SPA on NTRU software implementation

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NTRU

Post-quantum candidate for NIST standard

Authors: Jeffrey Hoffsteinem, Jill Pipherovou a Josephem H. Silvermanem (1996)

Polynomial ring $\mathcal{R} = \mathbb{Z}[x]/(x^N - 1)$

An element $f \in \mathcal{R}$ can be written as $f = \sum_{i=0}^{N-1} f_i x^i$

**Coprime integers p and q where $p \ll q$
(p is 3, q is 2048 or 4096)**

f_q is polynom f with reduced coefficients modulo q

Key Generation

f and g are random ternary polynomials with fixed
number of coefficients 1 and -1

$$h = pf_q^{-1}g \bmod q$$

f is the priv. key, h is the pub. key

Encryption

Input: random r , message m , public key h

$$e = rh + m \bmod q$$

Decryption

Input: ciphertext e , private key f

$$a = fe \bmod q$$

$$m = af_p^{-1} \bmod p$$

Remark: f is chosen so that f_p^{-1} equals 1

Correctness

$$a = e \cdot f \bmod q$$

$$\stackrel{e=rh+m}{=} r \cdot h \cdot f + m \cdot f \bmod q$$

$$\stackrel{h=pf_q^{-1}g}{=} r \cdot pf_q^{-1}g \cdot f + m \cdot f \bmod q$$

$$= pr \cdot g + m \cdot f \bmod q$$

$$a \cdot f_p^{-1} = (pr \cdot g + m \cdot f) \cdot f_p^{-1} \bmod p$$

$$= 0 + m \cdot f \cdot f_p^{-1} \bmod p$$

$$= m \bmod p$$

Power Analysis

We want to find the secret key f ...

... by observing power consumption of the device

We look at the decryption algorithm (where private key is used)

Essentially, it is just a polynomial multiplication modulo q .

Implementation

Suggested in: An, Soojung, Suhri Kim, Sunghyun Jin, Hanbit Kim and Hee-Seok Kim. "Single Trace Side Channel Analysis on NTRU Implementation." Applied Sciences 8 (2018): 2014

**Implementation was proposed as a countermeasure against power analysis
(dispose of leakage based on different instructions)**

**Our code based on StrangSwan implementation
(<https://www.strongswan.org/>)**

The private key is sparse (values 1,-1 or 0)

The implementation use sparse encoding b of the private key (saves the space)

Stores increasing coefficients of 1 and -1

Assume $f = 1 - x^1 + x^2 + x^3 - x^4 + x^7 - x^8$, then we encode it as $b = [0, 2, 3, 7, 1, 4, 8]$.



Require: cipher-text polynomial $e \in R$ and encoding b of private key F

Ensure: $H = Fe \pmod{q}$

1. **for** $i = 0; i < N; i++$
2. $t_i \leftarrow r$, r is a random value
3. **end for**
4. **for** $j = d_f + 1; j < 2d_f + 1; j++$ **do**
5. $k \leftarrow b_j$
6. $y \leftarrow N - k$
7. **for** $i = 0; i < N; i++, y++$ **do**
8. $t_i \leftarrow t_i + e_y$
9. **end for**
10. **end for**
11. **for** $i = 0; i < N; i++$ **do**
12. $t_i \leftarrow -t_i$
13. **end for**
14. **for** $j = 0; j < d_F + 1; j++$ **do**
- ⋮

Visualization

$$\begin{array}{rcl}
 (e_{N-1}e_{N-2} \cdots e_1e_0) & \times & (e_{N-1}e_{N-2} \cdots e_1e_0) \\
 & & b_{2*df} \cdots b_{df+1}b_{df} \cdots b_1b_0 \\
 \hline
 & r & \cdots & r \\
 & & + e_{N-b_{df+1}} & | \text{ initialization step of array } t \\
 & & + e_{N-b_{df+1}+1} & | \text{ step 1.0, } b_{df+1} (=k) \\
 & & + e_{N-b_{df+1}+2} & | \text{ step 1.1, } b_{df+1} \\
 & & \cdots & | \text{ step 1.2, } b_{df+1} \\
 & & + e_{2*N-b_{df+1}-1} & | \vdots \\
 & & & | \text{ step 1.N, } b_{df+1} \\
 & & + e_{N-b_{df+2}} & | \text{ step 2.0, } b_{df+2} (=k) \\
 & & \cdots & | \vdots
 \end{array}$$

ciphertext stored in doubled way

Where is the leakage

$$\begin{pmatrix} e_{N-1} & \dots & 0 & \dots & e_0 \end{pmatrix} \times \begin{pmatrix} e_{N-1} & \dots & 0 & \dots & e_0 \\ b_{2*df} & \dots & b_{df} b_{df-1} & \dots & b_1 b_0 \end{pmatrix} \quad | \quad \text{Lets say there is a zero...}$$

This zero slowly shifts to the left...

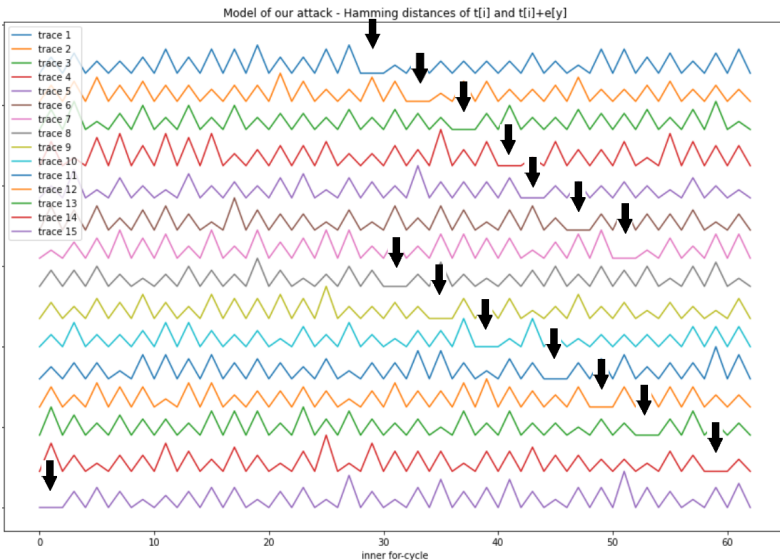
	r	r	\dots	r	r		initialization step of array t
+	e_{N-k-1}		\dots	0	\dots	e_{N-k}	step 1, $b_{df+1}(=: k)$
+	e_{N-k-1}		\dots	0	\dots	e_{N-k}	step 2, $b_{df+2}(=: k)$
+	e_{N-k-1}	\dots	0	\dots		e_{N-k}	step 3, $b_{df+3}(=: k)$
							\vdots step 4, $b_{df+4}(=: k)$

This can be seen in power trace...



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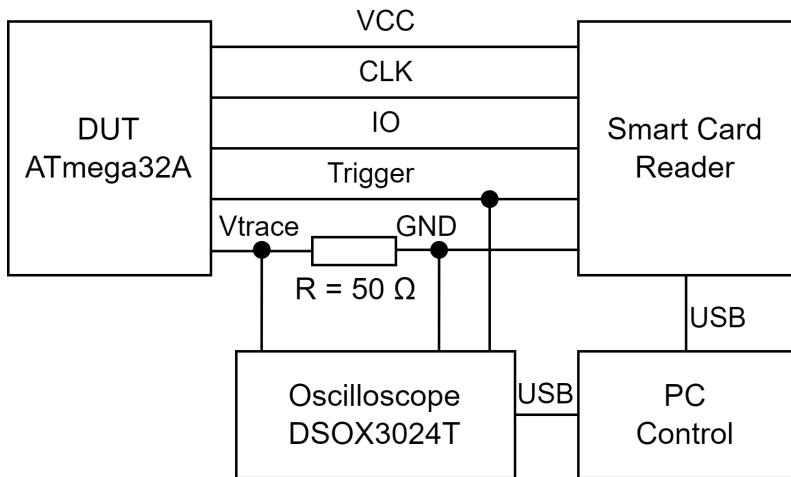


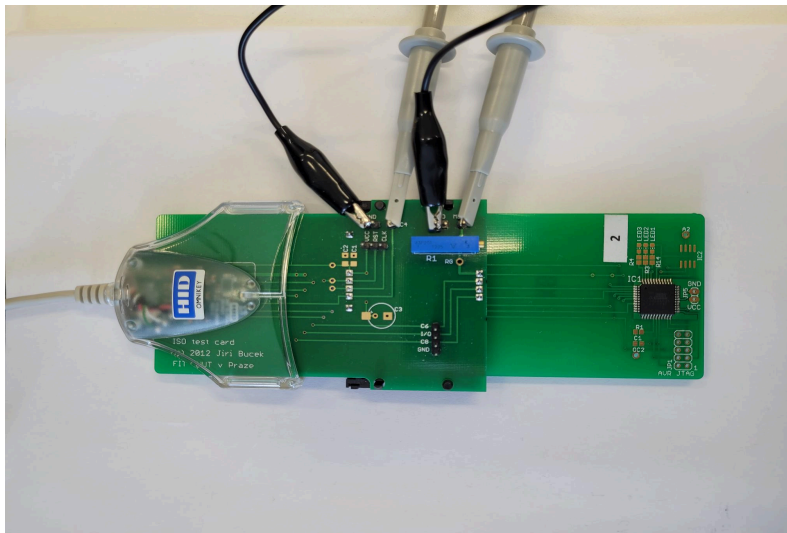
Target

8-bit microcontroller of the Microchip AVR family, namely ATmega32A

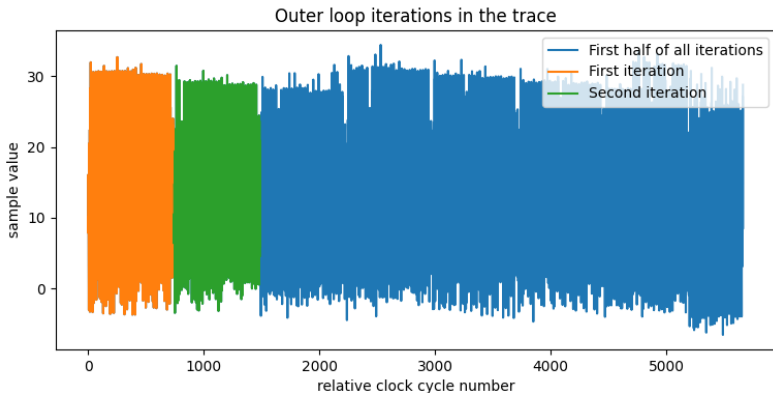
50 Ω series resistor using a standard passive oscilloscope probe

Keysight DSOX3024T oscilloscope connected to the controlling PC via USB





Experimental results



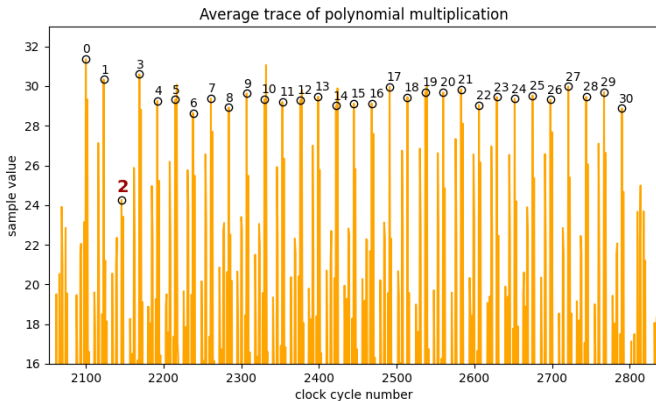
How to get rid of noise...

We use more traces and compute mean from them

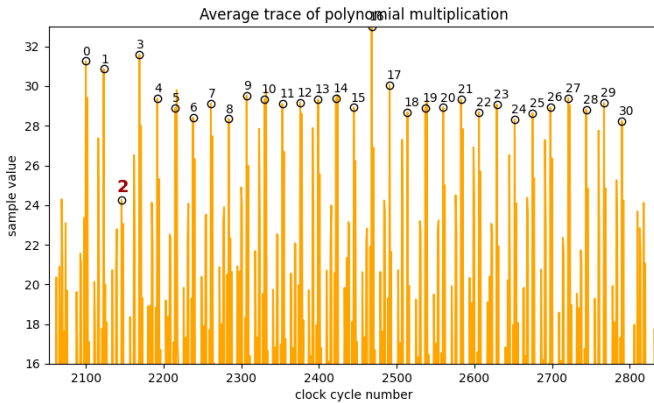
We choose convenient ciphertext as 0 following with high hamming weight values (255)

Then we get nice results :)

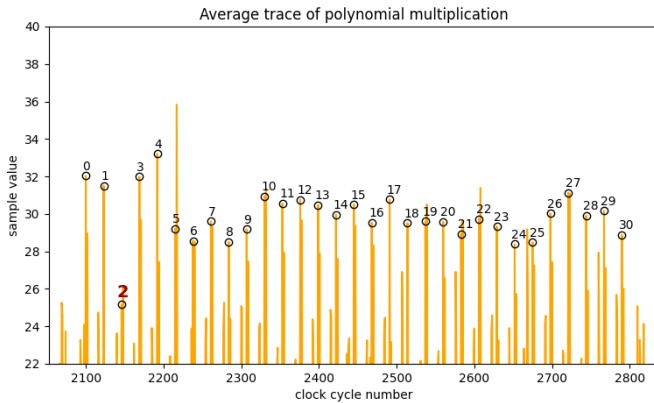
10 traces average



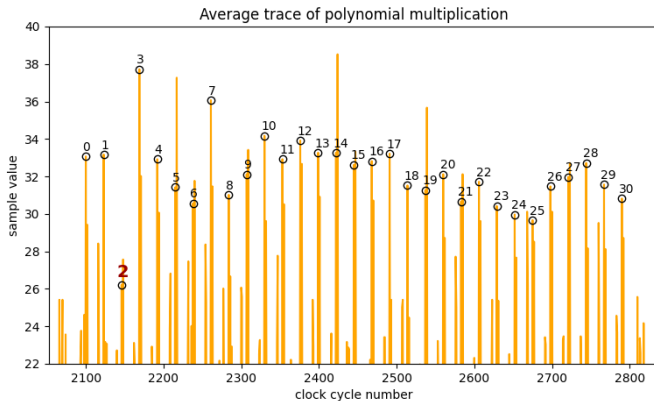
4 traces average



2 traces average



1 trace



Conclusion

We discovered leakage depending on input data in the protected implementation

Successfully verified by experiment for chosen ciphertext on AVR 8-bit microcontroller as the target

Future work?

Use 32-bit microcontroller

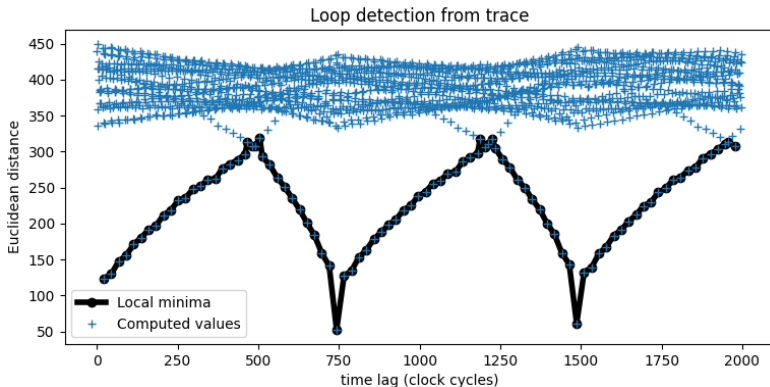
Use real value parameters (not smaller)

Combine with classical cryptoanalysis to achieve good results even for random ciphertext and just one trace

Thanks for attention

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Appendix



Bugs in original implementation:

Parity of N

Subtraction of r in the end

Indices out of range

-> double array e



Algorithm 4 Countermeasure of NTRU Open Source Project

Require: cipher-text polynomial $e \in R$ and coefficient location indices of private key b

Ensure: $H = F \cdot e \pmod{q}$

```
1: for  $i = 0; i < N; i++$  do
2:    $t_i \leftarrow r$  ▷  $r$  is a random value
3: end for
4: for  $j = d_f + 1; j < 2d_f + 1; j++$  do
5:    $k \leftarrow b_j$ 
6:    $x \leftarrow \frac{N-1}{2} - k, y \leftarrow N - k$ 
7:    $t_{\frac{N-1}{2}} \leftarrow t_{\frac{N-1}{2}} + e_x$ 
8:    $x \leftarrow x + 1$ 
9:   for  $i = 0; i < \frac{N-1}{2}; i++, x++, y++$  do
10:     $t_{\frac{N}{2}+i+1} \leftarrow t_{\frac{N}{2}+i+1} + e_x$ 
11:     $t_i \leftarrow t_i + e_y$ 
12:   end for
13: end for
14: for  $i = 0; i < N; i++$  do
15:    $t_i \leftarrow -t_i$ 
16: end for
17: for  $j = 0; j < d_F + 1; j++$  do
18:    $k \leftarrow b_j$ 
19:    $x \leftarrow \frac{N-1}{2} - k, y \leftarrow N - k$ 
20:    $t_{\frac{N-1}{2}} \leftarrow t_{\frac{N-1}{2}} + e_x$ 
21:    $x \leftarrow x + 1$ 
22:   for  $i = 0; i < \frac{N-1}{2}; i++, x++, y++$  do
23:     $t_{\frac{N}{2}+i+1} \leftarrow t_{\frac{N}{2}+i+1} + e_x$ 
24:     $t_i \leftarrow t_i + e_y$ 
25:   end for
26: end for
27: for  $i = 0; i < N; i++$  do
28:    $H_i \leftarrow t_i - r \pmod{q}$ 
29: end for
30: return  $H$ 
```
